

REMARKS

The specification has been amended to provide a cross-reference to the previously filed International Application. The specification has also been amended to correct a typographical error. Entry of the present amendment and favorable action on the above-identified application are earnestly solicited.

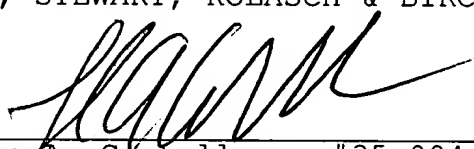
Attached hereto is a marked-up copy of the changes made to the application by this Amendment.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. § 1.16 or under 37 C.F.R. § 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

By


John A. Castellano, #35,094

P.O. Box 747
Falls Church, VA 22040-0747
(703) 205-8000

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Attachment: Version With Markings Showing Changes Made

(Rev. 01/22/01)

VERSION WITH MARKINGS SHOWING CHANGES MADE

The specification has been amended to provide cross-referencing to the International Application. The specification has also been amended as follows:

In the decoder shown in Fig. 1([a]b), reference numeral 11 denotes a first decoder for calculating the logarithm likelihood ratio from receiving signal: L_{cy} (corresponding to receiving signals: V_0 , V_1 , W_0 and W_1 , as will be described later), 12 and 16 are adders. Reference numerals 13 and 14 denote interleavers. Reference numeral 15 denotes a second decoder for calculating the logarithm likelihood ratio from the receiving signal: L_{cy} (corresponding to receiving signals: V_0 , V_1 , W_0 and W_1 , as will be described later). Reference numeral 17 denotes a deinterleaver. Reference numeral 18 denotes a first judging device for judging the output of the second decoder 15 to output an estimated value of the original information bit list, and reference numeral 19 denotes a second judging device for hard-judging the L_{cy} (corresponding to receiving signals: $V_2...$, $W_2...$, as will be described later) to output an estimated value of the original information bit list.